09811344_CLSTITLES
Titles of Most Frequently Occurring Classifications of Patents Returne d

From A Search of 09811344 on November 22, 2004

. 5		370	: MULTIPLEX COMMUNICATIONS
4		398	OR, 4 XR) : OPTICAL COMMUNICATIONS MULTIPLEX .Optical switching
	370/396 Class 370/351 370/389 370/395.	370 1	: MULTIPLEX COMMUNICATIONS PATHFINDING OR ROUTING .Switching a message which includes an address header
3		370	
3	709/226 Class		OR, 2 XR) : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING SYSTEMS: MULTIPLE COMPUTER OR PROCESS COO
RDIN	709/200 709/223 709/226		MULTICOMPUTER DATA TRANSFERRING .Computer network managingNetwork resource allocating
2	340/825	340	: COMMUNICATIONS: ELECTRICAL
2		370	: MULTIPLEX COMMUNICATIONS

09811344 CLSTITLES served by respective fixed stations .. Contiguous regions interconnected by a local 370/338 area network 370/360 (0 OR, 2 XR)2 Class 370 : MULTIPLEX COMMUNICATIONS 370/351 PATHFINDING OR ROUTING 370/357 .Through a circuit switch .. Switching control 370/360 370/401 (0, OR, 2XR)2 370 : MULTIPLEX COMMUNICATIONS Class 370/351 PATHFINDING OR ROUTING .Switching a message which includes an address 370/389 header .. Having a plurality of nodes performing 370/400 distributed switching 370/401 ...Bridge or gateway between networks (0 OR, 2 XR)2 370/403 370 : MULTIPLEX COMMUNICATIONS Class PATHFINDING OR ROUTING 370/351 370/389 .Switching a message which includes an address header .. Having a plurality of nodes performing 370/400 distributed switching 370/401 ...Bridge or gateway between networks 370/402Bridge between bus systems 370/403At least one bus is a ring network 370/404 (0 OR, 2 XR)2 Class 370: MULTIPLEX COMMUNICATIONS 370/351 PATHFINDING OR ROUTING 370/389 .Switching a message which includes an address header 370/400 .. Having a plurality of nodes performing distributed switching 370/401 ...Bridge or gateway between networksBridge between bus systems 370/402 370/403At least one bus is a ring network 370/404Ring or loop forms backbone for interconnecting other networks

PATHFINDING OR ROUTING

.Switching a message which includes an address

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370: MULTIPLEX COMMUNICATIONS

header

(0 OR, 2 XR)

370/410

Class

370/351

370/389

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	370/400		09811344_CLSTITLESHaving a plurality of nodes performing distributed switching		
	370/410		Having a signaling feature		
2	370/351 370/428	370	OR, 1 XR) : MULTIPLEX COMMUNICATIONS PATHFINDING OR ROUTING .Store and forwardParticular storing and queuing arrangement		
2	370/464	370	OR, 1 XR) : MULTIPLEX COMMUNICATIONS COMMUNICATION TECHNIQUES FOR INFORMATION CARRIED IN PLURAL CHANNELS		
	370/465 370/470 370/471		.Adaptive Frame length Message having an address header		
2	370/474 Class 370/473	370	: MULTIPLEX COMMUNICATIONS		
	370/474		.Assembly or disassembly of messages having address headers '		
2	370/905 Class 370/901 370/902 370/903 370/905	370	OR, 2 XR) : MULTIPLEX COMMUNICATIONS WIDE AREA NETWORK .Packet switchingOSI Compliant NetworkAsynchronous Transfer Mode (ATM)		
2	375/260 Class 375/259	375	OR, 2 XR) : PULSE OR DIGITAL COMMUNICATIONS SYSTEMS USING ALTERNATING OR PULSATING CURRENT		
	375/260		.Plural channels for transmission of a single pulse train		
2	709/200 Class	•	OR, 1 XR) : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING SYSTEMS: MULTIPLE COMPUTER OR PROCESS COO		
RDINATING					
	709/200		MULTICOMPUTER DATA TRANSFERRING		
2	710/33 Class		OR, 2 XR) : ELECTRICAL COMPUTERS AND DIGITAL DATA		

PROCESSING SYSTEMS: INPUT/OUTPUT

	710/1 710/33		09811344_CLSTITLES INPUT/OUTPUT DATA PROCESSING .Data transfer specifying
2	710/36 Class 710/1 710/36	710	OR, 2 XR) : ELECTRICAL COMPUTERS AND DIGITAL DATA PROCESSING SYSTEMS: INPUT/OUTPUT INPUT/OUTPUT DATA PROCESSING .Input/Output access regulation
2	710/5 Class 710/1 710/5	•	OR, 1 XR) : ELECTRICAL COMPUTERS AND DIGITAL DATA PROCESSING SYSTEMS: INPUT/OUTPUT INPUT/OUTPUT DATA PROCESSING .Input/Output command process
2	711/111 Class 711/100 711/101 711/111	711	OR, 1 XR) : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING SYSTEMS: MEMORY STORAGE ACCESSING AND CONTROL .Specific memory compositionAccessing dynamic storage device
2	711/112 Class 711/100 711/101 711/111 711/112	711	OR, 2 XR) : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING SYSTEMS: MEMORY STORAGE ACCESSING AND CONTROL .Specific memory compositionAccessing dynamic storage deviceDirect access storage device (DASD)